

## Fabrication of palladium-based microelectronic devices by microcontact printing

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This letter demonstrates the patterning of thin films of metallic palladium by microcontact printing ( $\mu$ CP) of octadecanethiol, and the use of the patterned films in the fabrication of a functional sensor. This technique was also used to prepare templates of palladium for the electroless deposition of copper. The resistivity of the palladium and copper microstructures was 13.8 and 2.8  $\mu\Omega$  cm, respectively; these values are approximately 40% larger than the values for the pure bulk metals. Palladium patterned into serpentine wires using  $\mu$ CP functioned as a hydrogen sensor with sensitivity of 0.03 vol %  $H_2$  in  $N_2$ , and a response time of  $\sim 10$  s (at room temperature). © 2002 American Institute of Physics. [DOI: 10.1063/1.1463719]

Microcontact printing ( $\mu$ CP) of long-chain alkanethiols on gold films forms self-assembled monolayers (SAMs) of alkanethiolates. Selective removal of the unprotected regions of the films by etching provides a useful, nonphotolithographic method for the fabrication of conducting gold microstructures.<sup>1</sup> The structures generated by this procedure have pinhole defects, and their edges have a roughness of  $\sim 100$ – $150$  nm. The defects in the etched microstructures are probably due to defects in the SAMs; the roughness of the edges seems to correlate with the size of the grains produced by e-beam deposition of gold.<sup>2</sup> Gold has the additional disadvantage as a substrate that it is not compatible with many technologies for fabrication of semiconductor-based microelectronic devices.<sup>3</sup> These characteristics limit the applications of  $\mu$ CP on gold in the microfabrication of electronic and optical devices.

To fabricate electronically functional devices using  $\mu$ CP, we needed a system that could generate microstructures with fewer functionally significant defects and smaller grain sizes than those produced using alkanethiolates on gold, and that was compatible with complementary metal–oxide semiconductor (CMOS) technologies. Here, we describe the results of a study of  $\mu$ CP of alkanethiolates on palladium.<sup>4</sup> Our initial evidence indicates that this system does, in fact, allow production of microstructures with substantially higher quality than those generated using gold. We believe that this fabrication process will be directly CMOS compatible.

Soft lithography is a suite of techniques, including  $\mu$ CP, whose members are useful for patterning of SAMs of thiol-containing organic molecules (RSH) onto surfaces.<sup>1</sup> For  $\mu$ CP, an elastomeric stamp made of poly(dimethylsiloxane) (PDMS) with bas-relief features is coated with an alkanethiol and placed in contact with a metal surface. This process leaves SAMs where the stamp contacted the metal. If the group R has an appropriate structure, the patterned SAMs act as a resist and protect the underlying metal films from wet-chemical etchants.<sup>1</sup>

Palladium shares three desirable properties with gold for use in  $\mu$ CP. First, it is resistant to spontaneous formation of surface oxides (up to 400 °C).<sup>5</sup> Second, it can be evaporated onto surfaces using standard techniques.<sup>6</sup> Third, it reacts readily with alkanethiols to form well-ordered monolayers. These SAMs are analogous to those formed on gold and silver.<sup>4</sup> The resulting Pd/SR surface is a resist for wet-chemical etchants; etching yields structures with smaller etch pits than are observed with SAMs on gold.<sup>4,7</sup> Palladium in films prepared by e-beam evaporation has a typical grain size ( $\sim 15$ – $20$  nm) that is smaller than that of gold ( $\sim 40$ – $75$  nm);<sup>8</sup> this characteristic leads to etched patterns of palladium with smaller roughness than etched patterns of gold. Palladium is also catalytically active, and the ability to pattern it may be useful in catalysis.<sup>9,10</sup> Although the resistivity of palladium is a factor of 5 greater than that of gold, palladium is used commonly in microelectronic devices such as capacitors, resistors, and contacts. Patterned films and colloids of palladium serve as seed layers for the electroless deposition of copper to form electrical interconnects.<sup>9,11,12</sup> Many alloys of palladium are used routinely in microelectronic devices—e.g., in resistors (AgPd) and finishes (NiPd and CoPd).<sup>5</sup>

We prepared the substrates by e-beam evaporation of titanium (2 nm, 1 Å/s, as an adhesion layer) and then palladium (25 nm, 8 Å/s) onto a 3 in. Si/SiO<sub>2</sub> <100> wafer (Silicon Sense, NH).<sup>13</sup> We used rapid prototyping techniques<sup>1</sup> to generate the stamps for  $\mu$ CP made of PDMS (Sylgard 184, Dow Corning; Midland, MI). The stamp was coated with C18SH by wiping a cotton swab wet with an ethanolic solution of octadecanethiol (5 mM) across the surface. After wetting with C18SH, the stamp was then dried under a stream of  $N_2$  for 1 min. The stamp was placed in contact with the palladium surface, allowed to wet the surface without additional pressure for 3–7 s, and then removed manually. The unprotected palladium was removed by etching in a commercially available FeCl<sub>3</sub>-based etchant (TFP, Transene, Inc.; Danvers, MA) that had been diluted to a ratio of H<sub>2</sub>O:etchant of 5:1.<sup>14</sup>

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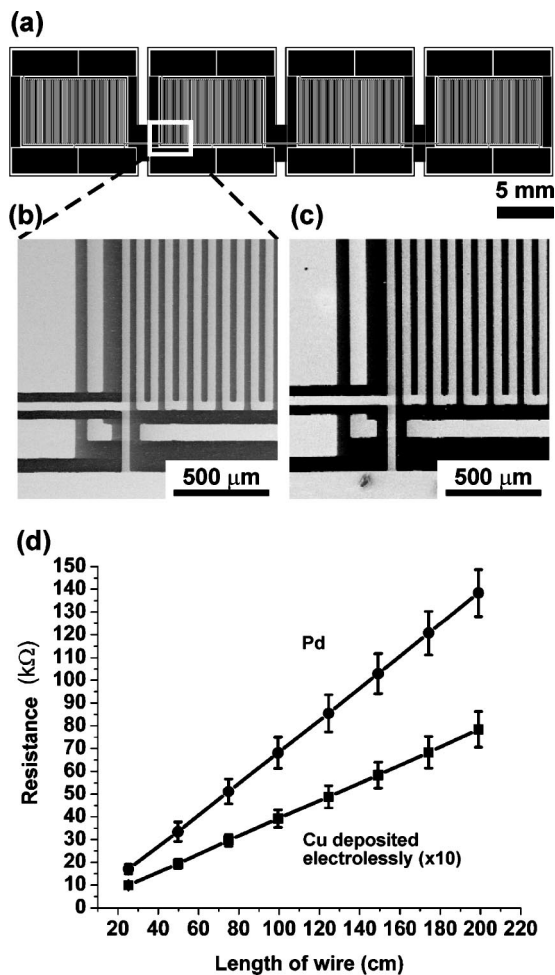


FIG. 1. (a) Design of the wire and contact pad system. Each contact pad is connected to the wire at different locations along its length. The individual sections of wire are all connected to each other. (b) Scanning electron microscope (SEM) image of a section of the palladium wire (42.5  $\mu\text{m}$  wide and 25 nm thick) generated using the pattern in (a). (c) SEM image of a section of the copper wire (45  $\mu\text{m}$  wide and 170 nm thick) deposited electrolessly onto the patterned, thin film of palladium. (d) Resistance measured as a function of length on both the palladium and copper wire.

We printed octadecanethiol on palladium, and etched to fabricate a long wire ( $\sim 2$  m; width of 42.5  $\mu\text{m}$ ; thickness of 25 nm) with contact pads spaced along the length of the wire to facilitate the measurement of the resistance as a function of length [Figs. 1(a) and 1(b)]. The underlying substrate was a thermally grown  $\text{SiO}_2$  layer (200 nm). The resistance was measured by a two-probe technique. The resistance values depended linearly on length [Fig. 1(d)]. The average resistivity was calculated to be 13.8  $\mu\Omega$  cm; the literature value for the resistivity of bulk palladium is 10.8  $\mu\Omega$  cm.<sup>5</sup> We also deposited copper electrolessly on this palladium wire.<sup>15</sup> The palladium wire from Fig. 1(b) was immersed in aqueous HF (1 vol %) for 10 min to remove the exposed Ti and then cleaned in an oxygen plasma for 30 s to remove the alkanethiolates from the surface. The cleaned wire was immersed in an electroless-plating solution<sup>16</sup> for 30–240 s.<sup>11</sup> Figure 1(c) indicates that the copper structures produced by the technique are restricted to the template made of palladium; they do not possess visible defects, and they have electrical characteristics consistent with those expected from bulk metal. Figure 1(d) plots the resistance of the copper wire measured as a function of its length. The average resistivity was 2.8  $\mu\Omega$  cm; the literature values for the resistivity of bulk and electrolessly plated copper are 1.7 and 2  $\mu\Omega$  cm, respectively.<sup>5,11</sup>

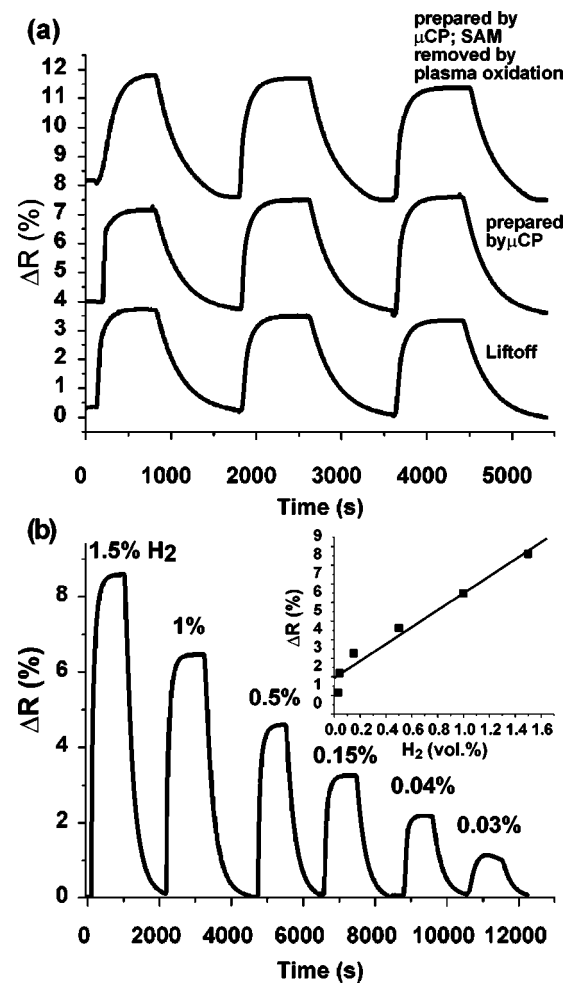


FIG. 2. Resistance measured as a function of time of the palladium wire from Fig. 1(b) when exposed to a  $\text{H}_2/\text{N}_2$  mixture at 25  $^\circ\text{C}$ . (a) Response of palladium wires that were prepared by  $\mu\text{CP}$  and by lift-off to exposure to 0.5 vol %  $\text{H}_2$  in  $\text{N}_2$ . The  $\text{H}_2/\text{N}_2$  mixture was turned on at 60, 1800, and 3600 s. The pure  $\text{N}_2$  was turned on at 1000, 2800, and 4600 s. (b) Response of a palladium wire prepared by  $\mu\text{CP}$  and subsequent removal of the SAM to decreasing concentrations by volume of  $\text{H}_2$  in  $\text{N}_2$ . The inset is a plot of  $\Delta R$  as a function of  $\text{H}_2$  concentration.

Palladium can be used to detect trace amounts of  $\text{H}_2$ ,<sup>17</sup> because palladium reacts reversibly with hydrogen gas to form  $\text{PdH}_x$  at room temperature or above;<sup>18</sup> this reaction results in changes in its physical properties—resistivity and reflectivity—that can be measured easily.<sup>17,19</sup> Hydrogen-based fuel cells (among other applications) have spurred interest in the development of inexpensive sensors of  $\text{H}_2$  in both the research and commercial sectors.<sup>20,21</sup> Mainstream adoption of  $\text{H}_2$ -based fuel cell technology requires devices that can detect small  $\text{H}_2$  concentrations ( $< 4$  vol %) rapidly.<sup>22</sup> The palladium wire [Figs. 1(a) and 1(b)] was placed in a flow chamber and electrical leads were connected to the contact pads. The wire was exposed to a stream of gas (either a  $\text{H}_2/\text{N}_2$  mixture or pure  $\text{N}_2$ ), and the resistance was monitored. Figure 2(a) shows that the magnitude of the change in resistance ( $\sim 3.5\%$   $\Delta R$ ) and the response time ( $\sim 10$  s) as a function of exposure to  $\text{H}_2$  for palladium wires prepared by  $\mu\text{CP}$  [either with or without a SAM (Ref. 23) on their sur-

face] is similar to those of a palladium wire prepared by photolithography and lift-off.<sup>24</sup> All three samples had a slow initial response time ( $\sim 120$  s) for the first exposure to H<sub>2</sub>; subsequent exposures all had response times of  $< 10$  s. The sensor prepared by  $\mu$ CP detected H<sub>2</sub> concentrations as low as 0.03 vol % in N<sub>2</sub> [Fig. 2(b)] with sensitivity equivalent to that of other palladium-based devices for sensing H<sub>2</sub>.<sup>20</sup> These data show that the thin film of palladium retains its bulk ability to adsorb H<sub>2</sub> after patterning by  $\mu$ CP of alkanethiolates with no increase in response time or decrease in sensitivity.

Sulfur is known to poison the catalytic activity of palladium and affect the response time of palladium-based H<sub>2</sub> sensors, and it seemed possible that exposure to H<sub>2</sub> might influence the interface between alkylthiolates and palladium, and thus the SAM.<sup>17</sup> Infrared spectra of a SAM of octadecanethiol on palladium show no change in frequency (within  $\pm 1$  cm<sup>-1</sup>) or intensity ( $\pm 10\%$ ) after exposure to H<sub>2</sub> (0.5 vol % in N<sub>2</sub>) for 10 min (see supplemental information). We infer that exposure of the SAM to H<sub>2</sub> does not change it significantly. Thus, the RS/Pd interface seems to be transparent to H<sub>2</sub>.

The ability to pattern palladium films directly by  $\mu$ CP extends the utility of the soft-lithographic technique in generating functional micro- and nanostructures. We believe that palladium is superior to gold for use with  $\mu$ CP in the fabrication of devices for five reasons. (i) The features produced by  $\mu$ CP on palladium have a relatively low density ( $\sim 70$  defects/mm<sup>2</sup>) of small pits ( $< 1$   $\mu$ m<sup>2</sup>) and have relatively smooth edges (30–50 nm). (ii) Palladium is commonly used as a substrate for electroless deposition of copper and other metals. (iii) Many alloys of palladium are already used as finishes to connectors and as precision resistors in microelectronic devices. (iv) The chemical etchant used for palladium is less toxic than the Fe(CN)<sub>6</sub><sup>3+</sup> etch used for gold (which can generate HCN if treated incorrectly). (v) Palladium is catalytically active, and the ability to pattern palladium by  $\mu$ CP may be useful in generating substrates for use in catalysis.<sup>25</sup> The disadvantage of using palladium is that it is  $\sim 5$  times more resistive than gold (10.8  $\mu\Omega$  cm for palladium and 2.1  $\mu\Omega$  cm for gold).<sup>5</sup> We believe that fabrication of micro/nanostructures in palladium by soft lithography can be incorporated into the fabrication of Si-based microelectronic devices because palladium is CMOS compatible.

*Note added in proof.* Carvalho et al. at IBM in Zurich have carried out a similar study of the patterning of palladium by microcontact printing of alkanethiols. Although our inferences concerning the mechanism by which alkanethiolate SAMs confer protection against etching differ from theirs, we agree on the characteristics and utility of the method. See: A. Carvalho, M. Geissler, H. Schmid, B. Michel, and E. Delamar. Langmuir, 2002 (in press) (available on ASAP, <http://pubs.acs.org>).

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- <sup>7</sup>See EPAPS Document No. E-APPLAB-80-032212 for experiments that examine the defects in the Pd/SR system. This document may be retrieved via the EPAPS homepage (<http://www.aip.org/pubservs/epaps.html>) or from <ftp.aip.org> in the directory/epaps/. See the EPAPS homepage for more information.
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- <sup>14</sup>The dilution slows the rate of etching to a controllable rate (i.e., 10–30 s).
- <sup>15</sup>Palladium-based substrates for electroless deposition of copper were patterned previously by techniques that required lasers and positioning stages (see Refs. 9, 10, and 12). We, and others, have demonstrated the patterning of palladium colloid and palladium (II) catalytic precursors  $\mu$ CP (see Refs. 9 and 11). The direct patterning of palladium metal by  $\mu$ CP does not require special equipment or synthesis of precursors.
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